

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : **Sheng-Ming DENG, Ching-Lung TSENG**

Date : April 1, 2004

Serial No. :

Group Art Unit:

Filed :

For : **MULTI-LAYER CERAMIC CHIP VARISTOR DEVICE SURFACE
INSULATION METHOD**

Honorable Commissioner of Patents

and Trademarks

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a completed Form PTO-1449. List of prior art and Comments on any non-English-language references (if any) pursuant to Rule are submitted herewith.

Respectfully submitted

Applicants(s). Sheng-Ming Deng Tseng-Chinglung _____

Encl: PTO-1449 & References

FORM PTO-1449 (Substitute)				ATTY. DOCKET NO. <u>04/48-URS</u>		SERIAL NO.	
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Ching-Lung TSENG, Sheng-Ming DENG			
				FILING DATE		GROUP	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLAS S	FILING DATE IF APPROPRIATE
	AA	6,232,867	5/15/2001	Yoshida et al.	338	21	8/22/2000
	AB						
	AC						
	AD						
	AE						
FOREIGN PATENT DOCUMENTS							
	AF	TW447775	7/21/2001	C.Y.Lee et al	21	76	7/7/2000
	AG	EP0806780	12/11/1997	Ravindranathan	H01C1	034	9/5/1996
	AH						
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
	AO						
EXAMINER				DATE CONSIDERED			
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							